

VCXO 7050 SMD

Features:

- Low Power Consumption
- High Stability, Small profile
- Broad Voltage Control Range
- Mass production with well uniformity
- Custom build capability
- ISO9001:2008 & ISO 14001:2004 certificated
- ROHS compliant



Specification:

Parameter		Min	Typ.	Max	Unit	Condition	Note
Frequency Stability							
Frequency		70		160	MHz		CMOS/HCMOS
		61.44		700.0	MHz		LVPECL
Initial Frequency Accuracy		-10		+10	ppm	Vc=1.65V/@25°C,	
		-15		+15			
Supply Variation		-3		+3	ppm	Vcc±5%, @25°C	
Load Variation		-2		+2	ppm	CL±5%, @25°C	
Aging	First year	-3		+3	ppm		
Temperature Stability		-25		+25	ppm	-40°C~+85@25°C	
Start up time				20	ms		
Supply Voltage/Current							
Voltage Supply		3.135	3.3	3.465	V		
Operating Current				10	mA		
				30	mA		
Output Characteristics							
CMOS	Load		15		pF		
	Output Level(VOL)			10%Vcc	V		
	Output Level(VOH)	90%Vc c			V		
	Duty Cycle	45		55	%		
	Rise Time/ Fall Time			5	ns		

* Above specification subject to change without prior notice, please consult our sales @ www.crystal-bj.com



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LVPECL	Load		50Ω /15p		pF		
	Output Level(VOL)			1.65	V		
	Output Level(VOH)	2.2			V		
	Duty Cycle	45		55	%		
	Rise Time/ Fall Time		1		ns		
Phase Noise							
Phase Noise			-65		dBc/ Hz	@10Hz	For 100.00MHz operational frequency
			-95			@100Hz	
			-120			@1KHz	
			-140			@10KHz	
			-145			@100KHz	
Voltage Control Characteristics							
Control Voltage Range		0	1.65	3.3	V		
		0	2.5	5	V		
Frequency tuning range		-100		-150	ppm	Vc=0V	For 100.00MHz operational frequency
		-10		+10	ppm	Vc=1.65V	
		+100		+150	ppm	Vc=3.3V	
Slope		Positive					
Linearity		-10		+10	%		
Input Impedance		100			MΩ		

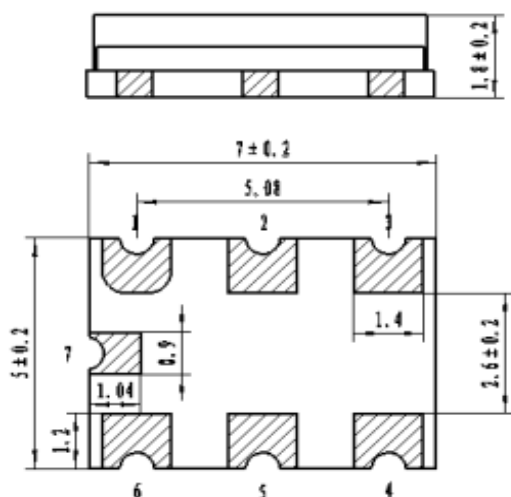
Environmental, Mechanical Conditions	
Operating temperature range	-40℃~+85℃
Operable temperature range	-40℃~+85℃
Storage temperature range	-50℃~+105℃
Drop Test	The test shall be carried out as the provisions of the IEC60028-2-32 test Ed. 10cm height, 3 times on hard board with thickness of 3cm
Bumping Test	Device are bumped to three mutually perpendicular axes at peak acceleration of 400m/s ² , each 4000±10 times , 6ms pulse duration time.
Vibration test	Frequency range: 1Hz-4Hz-100Hz-200Hz Acceleration: 0.0001g ² /Hz-0.01g ² /Hz-0.01g ² /Hz-0.001g ² /Hz Grms=1.15g Sweep time: 30 minutes (perpendicular axes each sweep time)
Mechanical Shock	100g, 6mS duration, 1/2 sine wave, 3 shocks each direction along 3 mutually perpendicular planes.
Thermal shock	0.5h@-40℃, 0.5h@+85℃, Note: the changing time < 30 seconds, cycling for 100 times

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Dimensions:

Unit:mm

Pin Function



Pin No.	Pin Function
1	VC
2	OE/NC
3	GND
4	OUTPUT1
5	OUTPUT2/NC
6	VCC
7	NC

Note1: Pin7 is NC, there is no electrical connection.

Note2: OE: Logic "1" or Floating input, outputs
Logic "0" ,Outputs Disable or customazition

How to Order

V7	Output	Supply Voltage	Temperature Range	OE	Voltage Control Range	Frequency
						Eg:100.000MHz
Code	Size(mm)	Code	Specification		Code	Specification
V7	SMD7050	3	3.3V ± 5%		100	≥ 100ppm
		5	5V ± 5%		120	≥ 120ppm
Code	Specification	Code	Specification	Code	Specification	
P	LVPECL	A	0°C to +70°C	H	Logic "1"	
H	HCMOS	B	-20°C to +70°C	L	Logic "0"	
L	LVC MOS	C	-40°C to +85°C	N	NC	